CLAIMS

What is claimed is:

1 1.   A processor comprising:

2   a voltage regulator to be powered by a first voltage and to provide a

3   second voltage; and

4   a circuit powered by the second voltage.

1 2.   The processor of claim 1, wherein the second voltage is adjustable by the

2   processor.

1 3.   The processor of claim 2, wherein the voltage regulator includes a digitized

2   resistor to be set by the processor.

1 4.   The processor of claim 1, wherein the second voltage is to be set to allow the

2   circuit to meet a timing requirement.

1 5.   The processor of claim 1, further comprising a port to receive the first voltage

2   from an external voltage regulator.

1 6.   The processor of claim 1, wherein the voltage regulator includes an op amp.

1 7.   The processor of claim 6, wherein the circuit is a digital circuit.

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1   8.   The processor of claim 1, wherein the circuit includes at least a portion of a

2   core of the processor.

1   9.   The processor of claim 1, wherein the circuit includes a memory region.

1   10.   The processor of claim 9, wherein the memory region is a cache.

1   11.   A computer system comprising:

2   a discrete voltage regulator to provide a global Vcc; and

3   a processor including a local voltage regulator to be powered by the

4   global Vcc and to provide a local Vcc for the processor.

1   12.   The computer system of claim 11, wherein the local Vcc is adjustable by the

2   processor.

1   13.   The computer system of claim 12, wherein the local voltage regulator

2   includes a digitized resistor to be set by the processor.

1   14.   The computer system of claim 11, wherein the processor includes a cache to

2   be powered by the local Vcc.

1   15.   The computer system of claim 11, wherein the processor is a graphics

2   controller.

1   16.   A method comprising:

2   providing a first voltage to a processor comprising an integrated voltage

3   regulator;

4   powering the voltage regulator with the first voltage, the voltage regulator

5   to provide a second voltage; and

6   powering at least a portion of the processor with the second voltage.

1   17.   The method of claim 16, further comprising adjusting the second voltage by

2   the processor.

1   18.   The method of claim 16, wherein powering at least a portion of the processor

2   includes powering a floating point unit of the processor.

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